ATSC Master™ II FD-RS PCIe

Overview

In North America and in certain other countries, SMPTE 310M has been designated as the standard for digital television and the delivery of MPEG-2 transport streams via 8VSB modulated signals. ATSC is the name of the organization that defined this specification, and SMPTE 310M is the standard which the transport stream must meet in order to interface to 8VSB modulators.

The ATSC Master II FD-RS is a PCIe I/O card that meets the SMPTE 310M electrical specifications, as well as the PCI Express bus specifications. It is designed to provide an interface for importing and exporting MPEG-2 transport streams from a PC. Please note that to be a valid ATSC transport stream, the stream must total 19.39 Mbps. It may be a single or a multi-program transport stream. Our card is designed to deliver such a stream with a very accurate clock rate and with the proper signaling methodology; thus we can claim to be SMPTE 310M compatible.

Please note that valid ATSC transport streams not only must meet encoding rate standards, but certain meta tables must be appended to the streams. The ATSC Master II FD-RS assumes that such tables are present and makes no alteration of the incoming or outgoing transport streams. It only looks at the data format, and the presence of a valid transport stream.

Features

- Windows® API and Linux® SDK
- Single Lane PCI Express interface
- Two SMPTE 310M Inputs -- Primary BNC and secondary via header
- Two buffered mirrored SMPTE 310M outputs
- Software control of primary or secondary input source selection
- Mechanical relay bypass on power failure
- Software controlled firmware loop-back bypasses the primary input to both outputs
- “Snoop” function for reading input data while in firmware bypass
- On-board circuitry to monitor transport stream quality
- Control Interface port
- 6 general purpose optically isolated inputs
- Status output for firmware loop-back
- Rx and Tx status indicators via LEDs
- External override input for the bypass relay
- Software readable, unique serial number on each board
- Field upgradeable firmware
- Software selectable transmit clock source
- External clock input, 38 MHz PECL signal

Applications

- Transporting of MPEG-2 over long distances meeting FCC requirements
- Moving MPEG-2 transport stream to/from the PCIe bus for processing
- Interfacing MPEG-2 to PCIe bus encoder/decoder boards
- Interfacing to video servers and general studio equipment
Specifications

<table>
<thead>
<tr>
<th>Input/Output</th>
<th>75-Ohm BNC Connector</th>
</tr>
</thead>
<tbody>
<tr>
<td>Coaxial Cable Type</td>
<td>75-Ohm impedance usable up to 850 MHz</td>
</tr>
<tr>
<td>External Clock Input</td>
<td>75-Ohm DC Blocked ECL</td>
</tr>
<tr>
<td>Data Input/Output</td>
<td>SMPTE 310M -1998 8VSB Transport Stream</td>
</tr>
<tr>
<td>Data Rate</td>
<td>19.39265846 Mbps</td>
</tr>
<tr>
<td>Data Input Voltage</td>
<td>800 mV ± 10%Peak</td>
</tr>
<tr>
<td>Data Input Offset</td>
<td>0.0 V ± 0.5 V</td>
</tr>
<tr>
<td>Bus Interface</td>
<td>Single Lane (x1) PCIe 1.0a</td>
</tr>
<tr>
<td>Card Size</td>
<td>6.875 x 4.2 inches (175 x 107 mm)</td>
</tr>
<tr>
<td>Voltage Requirements</td>
<td>+5 V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>0° to 55°C</td>
</tr>
<tr>
<td>Operating Humidity</td>
<td>To 90%, non-condensing</td>
</tr>
<tr>
<td>Clock Source Selection</td>
<td>Software controlled</td>
</tr>
</tbody>
</table>

Driver Support


Highlights

- New functions – mechanical bypass on power failure, secondary input, and watchdog timer
- Ideal for mission critical broadcast applications

Connector Diagram

- Input 1
- Receiver Active
- Primary Output
- Secondary Output
- Transmitter Active
- Clock Input
- Carrier Detect
- Sync Detect

Ordering Info

ATSC Master II FD-RS PCIe
(Model 138)